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Substitute for form 1449A/PTO

INFORMATION DISCLOSURE STATEMENT BY APPLICANT

Complete if Known

Application No.	09/748,098
Filing Date	December 21, 2000
First Named Inventor	Nicholas J. Kelsey
Art Unit	2183
Examiner Name	Charles A. Harkness
Attorney Docket Number	20880-05093

Sheet 1 of 1

U.S. PATENT DOCUMENTS

Examiner Initials*	Cite No. ¹	Document No. Number - Kind Code ² (if known)	Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document
CH	A1	US-6,728,722	04-27-2004	Shaylor
CH	A2	US-6,542,991	04-01-2003	Joy et al.
CH	A3	US-4,777,587	10-11-1988	Case et al.
		US-		
		US-		
		US-		
		US-		
		US-		

FOREIGN PATENT DOCUMENTS

Examiner Initials*	Cite No. ¹	Foreign Patent Document Country Code ³ - Number ⁴ Kind Code ⁵ (if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	T ⁶

OTHER REFERENCES - NON-PATENT LITERATURE DOCUMENTS

Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published	T ⁶
CH	A4	Intel Corporation, Pentium Processor Family Developer's Manual, Volume 3: Architecture and Programming Manual, 1995, pages 25-1 and 25-30.	

Examiner Signature		Date Considered	10/14/04
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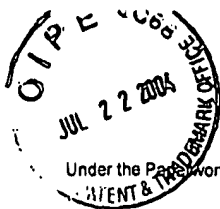
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Examiner Initials*	Cite No. ¹	Document No. Number - Kind Code ² (if known)	Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document
		US-		
		US-		
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FOREIGN PATENT DOCUMENTS				
Examiner Initials*	Cite No. ¹	Foreign Patent Document Country Code ³ - Number ⁴ Kind Code ⁵ (if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document

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CH	A	EGGERS, Susan J., "Simultaneous Multithreading: A Platform for Next-Generation Processors", IEEE Micro, Sept. Oct. 1997, pp. 12-19	

Examiner Signature		Date Considered	10/14/04
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